

## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandris, Viginia 22313-1450 www.isplo.gov

## 

Bib Data Sheet

**CONFIRMATION NO. 8259** 

SERIAL NUMBER 10/754,518	FILING DATE 01/12/2004 RULE	CLASS 257	GRO	GROUP ART UNIT 2814		ATTORNEY DOCKET NO. 60188-751		
APPLICANTS								
Yoshiharu Hidaka, Osaka, JAPAN;								
Katsuyuki Ikenouchi, Toyama, JAPAN;								
** CONTINUING DATA **********************************								
** FOREIGN APPLICATIONS ************************************								
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 04/13/2004								
Foreign Priority claimed	STATE OF	R SHI	EETS	тот	AL	INDEPENDENT		
35 USC 119 (a-d) conditions				DRAWING CĽA 12 1		MS	CLAIMS 2	
ADDRESS Jack Q. Lever, Jr. McDERMOTT, WILL & EMERY 600 Thirteenth Street, N.W. Washington, DC 20005-3096								
TITLE Semiconductor substrate, method for fabricating the same, and method for fabricating semiconductor device								
	EES: Authority has been given in Paper lo to charge/credit DEPOSIT ACCOUNT lo for following:			All Fees				
FILING FEE FEE				1.16 Fees (Filing)				
RECEIVED No.				1.1 time )	1.17 Fees (Processing Ext. of time)			
770				1.18 Fees (Issue)				